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The Gazette of the Democratic Socialist Republic of Sri Lanka
EXTRAORDINARY

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PART I : SECTION (I) — GENERAL

Government Notifications

L.D-B. 21/1978 (X) (A)

THE UNIVERSITIES ACT, No. 16 OF 1978

Order Under Section 25A

BY virtue of the powers vested in me by section 25A of the Universities Act, No. 16 of 1978, I, Professor G.L. Peiris, the Minister of Education, do by this Order, amend the Order made under the aforesaid section and published in Gazette Extraordinary No. 1995/39 of December 2, 2016, by the repeal of the contents under the heading “Conditions” in Schedule I and the substitution therefore of the following: -

“The minimum entry qualifications for admitting a student to follow any degree programme specified in Schedule II offered by SLT Campus (PVT) LTD shall be three (3) simple (S) passes in the Physical Science subject stream in one and the same sitting at the GCE (Advanced Level) Examination conducted by the Department of Examinations of Sri Lanka or an equivalent qualification, in accordance with the guidelines issued by the University Grant Commission.

Provided however, in respect of the Degree Programmes titled the Bachelor of Science Honours in Engineering in Electronics and Power Systems and the Bachelor of Science Honours in Engineering in Electronics and Telecommunications, specified in Schedule II, in addition to the above mentioned minimum entry qualifications, a student may be admitted to follow the respective Degree Programme if such student has successfully completed -

- either one of the qualifying lateral entry programmes; and
- all modules as required to fill any gap to gain the approved credits, prior to the fifth semester of the relevant Degree Programme;

as follows:-

1A- PG 5524 – 520 (08/2021)

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<i>Degree Programme</i>	<i>Qualifying lateral entry Programme</i>	<i>Modules to be offered for Gap-filling</i>		
		<i>Module Title</i>	<i>Credits</i>	<i>Semester of module</i>
1) Bachelor of Science Honours in Engineering in Electronics and Power Systems	National Diploma in Engineering Science (NDES) in the field of Power Engineering from the Institute of Technology, Katunayake (IET); or	EE2101 Signals and Systems	3	S3
		EM2201 Project Management	2	S4
	National Diploma in Technology (NDT) in the field of Electrical Engineering Technology from the Institute of Technology, University of Moratuwa (ITUM); or	EE 1201 Circuit Analysis II	3	S2
		EE2103 Digital Electronics III	3	S3
		EE2101 Signals and Systems	3	S3
		EE2202 Analog Electronics III	3	S4
		TE2201 Communication Systems	3	S4
		CE2201 Software Engineering	3	S4
		EM2201 Project Management	2	S4
	Graduate Diploma in Electrical Engineering from the Institution of Engineers Sri Lanka, College of Engineering (IESLCE).	EE 1201 Circuit Analysis II	3	S2
		EE2103 Digital Electronics III	3	S3
		EE2202 Analog Electronic III	3	S4
		EM2201 Project Management	2	S4

<i>Degree Programme</i>	<i>Qualifying lateral entry Programme</i>	<i>Modules to be offered for Gap-filling</i>		
		<i>Module Title</i>	<i>Credits</i>	<i>Semester of module</i>
2) Bachelor of Science Honours in Engineering in Electronics and Telecommunications	National Diploma in Engineering Science (NDES) in the field of Electrical, Electronics and Communication Engineering from the Institute of Technology, Katunayake (IET); or	Signals and Systems	3	S3
		Digital Electronics III	3	S3
		Project Management	2	S4
	National Diploma in Technology (NDT) in the field of Electronics and Telecommunication Engineering Technology from the Institute of Technology, University of Moratuwa (ITUM); or	Circuit Analysis II	3	S2
		Digital Electronics III	3	S3
		Analogue Electronics III	3	S4
		Signal and Systems	3	S3
		Project Management	2	S4
	Graduate Diploma in Electronics and Telecommunication from the Institution of Engineers Sri Lanka, College of Engineering (IESLCE).	Circuit Analysis II	3	S2
		Digital Electronics III	3	S3
		Analogue Electronics III	3	S4
		Project Management	2	S4 ⁷

Professor G. L. PEIRIS,
Minister of Education.

03rd August, 2021,
Colombo.

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